

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	3134448
<b>Application Number:</b>	10772293
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	4918
<b>Title of Invention:</b>	ETCHANT FOR SIGNAL WIRE AND METHOD OF MANUFACTURING THIN FILM TRANSISTOR ARRAY PANEL USING ETCHANT
<b>First Named Inventor/Applicant Name:</b>	Hong-Sick Park
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<b>Filer:</b>	Frank Chau
<b>Filer Authorized By:</b>	
<b>Attorney Docket Number:</b>	8071-112T
<b>Receipt Date:</b>	10-APR-2008
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<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$ 1743

RAM confirmation Number	1130				
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<b>File Listing:</b>					
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	8071-112T-PARTB.pdf	108367	no	1
			4d83793f255d14d241506a34ff6df64fbc982e7a		
<b>Warnings:</b>					
<b>Information:</b>					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8487	no	2
			3b1c4186446849032ba74fd4d16ebb18db6a673a		
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			116854		
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